1. The finished printed wiring board shall meet the requirements of IPC-A-600-X [latest revision].
2. This document and related artwork are computer generated. All modifications are to be performed to the original database on file.
3. This is a reduced dimension drawing. Additional dimensions needed to define the true profiles of this part shall be obtained from the Master CAD Model Database named 20019309_A.DIR. Dimensions and tolerances shall be interpreted per ASME Y14.5M. All dimensions are in mm.
4. Fabricate and inspect printed wiring board per IPC-6012/D, Class 2, Type 2.
5. Material: SOLA 370HR; 1 oz COPPER CLAD, DOUBLE SIDED. 1.5 mm THICK EPOXY/WOVEN GLASS LAMINATE PER IPC-4101/4. 26-1500-C1/C.
6. Surface finish to be Electrocite Nickel / Immersion Gold (ENIG) plating per IPC-4522A. All plated holes and conductive surfaces shall be plated with 0.025 mm Copper Minimum. All Ni/Au plated areas should have minimum thickness of 3µm for Ni and 0.051 µm for Au.
7. All holes specified in the drill chart are finished hole diameters. Hole tolerance +/- 0.051 mm for non-plated holes and +/- 0.076 mm for plated holes.
8. Fabrication tolerances: End product trace widths and lands shall not vary more than the smaller of 0.051 mm or 10% of the trace width from the Gerber data.
9. soldermask: Photo-imaged liquid polymer. Green color. Item 4, on both sides of board in accordance with IPC-610M-2, Type B, Class 2, over bare Copper.
10. Legend over solder mask on both sides of PCB using white non-conductive epoxy ink. Item 4.
11. Legend marking shall not be nearer than 0.127 mm to any pad. Character height shall be 0.75 mm minimum.
12. Bow and twist: Shall not exceed 0.75%
13. Electrical test: Continuity short and open testing on all available exposed terminal pads using IPC-D-356A netlist data. Continuity test shall be at 5 Ohms max. Shorts testing shall be performed at 250V. Minimum isolation of 20mA Ohms.
14. Mark in location shown with 1.0 mm minimum high characters the SERIAL NUMBER "XXXXXX" (WHERE N IS A VENDOR DESIGNATION LETTER AND XXXXXX IS A UNIQUE 5 DIGIT SERIAL NUMBER FOR EACH PART, THE VENDOR DESIGNATION LETTER AND THE STARTING SERIAL NUMBER SHALL BE IN ACCORDANCE WITH THE VALUES PROVIDED IN THE PURCHASE ORDER).
15. Cross-reference: TM1 153 400-02300, BOARD CONNECTOR/COAX ADAPTER PCB.

Dimensions (scale 2:1)

REV DESCRIPTION CAT DWN ENGR SEE JPL DATA MANAGEMENT SYSTEM FOR APPROVAL SIGNATURES AND DATES

A INITIAL RELEASE II - -

Layer stack legend

- Copper Top Overlay 0.008mm Signal G1
- Copper Bottom Layer 0.008mm Signal G1
- Nickel, Gold Bottom Surface Finish 0.005mm Surface Finish GBS
- Nickel, Gold Top Surface Finish 0.005mm Surface Finish GTS
- Copper Top Layer 0.008mm Signal G1
- Surface Material Top Solder 0.010mm Solder Resist Solder Mask GTO
- Nickel, Gold 0.005mm Surface Finish GTS
- Copper 0.0086mm Signal GTL
- 1.500mm FR-4 Dielectric

Total thickness: 1.601mm

Parts list

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<th>QTY</th>
<th>REFDES</th>
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<th>NOMENCLATURE OR DESCRIPTION</th>
<th>SPECIFICATION</th>
<th>MATERIAL OR NOTE</th>
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<td>B</td>
<td>D. PALMER</td>
<td>D. PALMER</td>
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Jet Propulsion Laboratory
California Institute of Technology
Pasadena, CA 91109
DRILL DRAWING VIEW (Scale 2:1)

DRILL TABLE

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<th>Symbol</th>
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<th>Plated</th>
<th>Hole Tolerance</th>
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-1 CONFIGURATION
DRILL DRAWING VIEW (Scale 2:1)

DRILL TABLE

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<th>Symbol</th>
<th>Count</th>
<th>Hole Size</th>
<th>Plated</th>
<th>Hole Tolerance</th>
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-2 CONFIGURATION