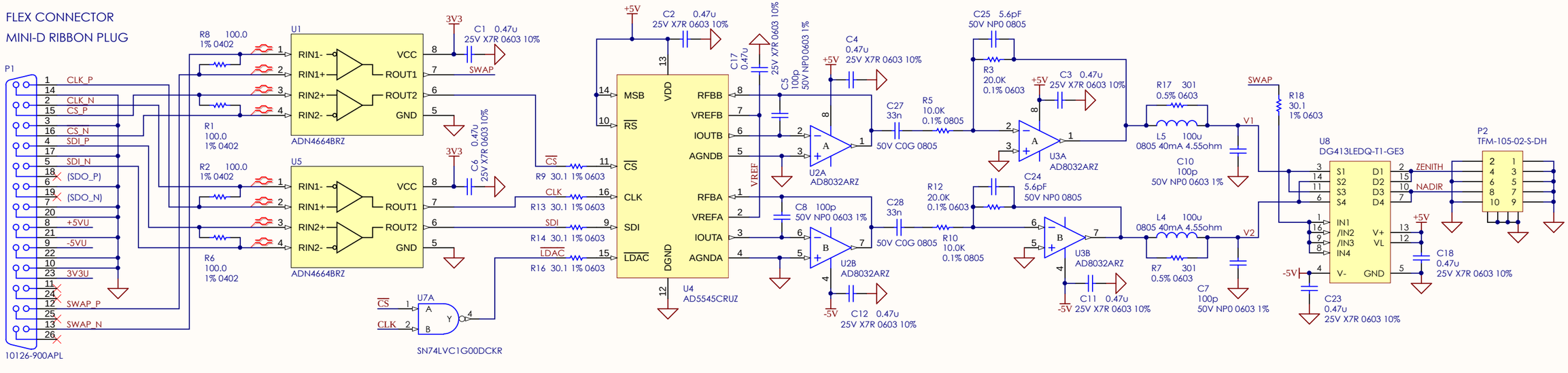
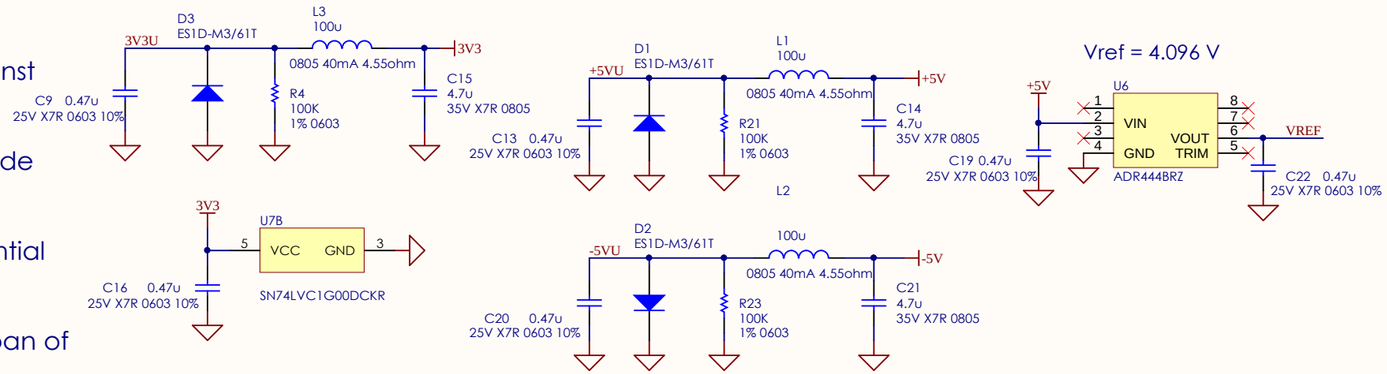
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REVISION HISTORY

| REV | DESCRIPTION | CAT | DWN | ENGR | SEE JPL DATA MANAGEMENT SYSTEM FOR APPROVAL SIGNATURES AND DATES |
|---|---|---|---|---|---|
| A | ---------- INITIAL RELEASE ---------- | II | | | |




NOTES

1. Place grounded guard trace around node [U3-6, R11-3], guarding against U3-7 node and U3-1 node.
2. Zero offset of height measurement is 19.5mm *k, where k = common-mode voltage / differential voltage
3. Resistor values shown give k in the range - 0.025 to +0.025, with a differential drive level of 0.5 of max. This gives a zero offset span of +/- 0.5 mm.
4. Reducing the differential drive level to 0.05 of max gives a zero offset span of +/- 5mm, with a proportional reduction in S/N.

DO NOT SCALE DRAWING
INTERPRET DEMENSIONING AND TOLERANCING PER ASME Y14.5 2009
INTERPRET DWG PER ASME Y14.100

| | |
|---|---|
| 10398553 | TMT |
| NEXT ASSEMBLY | USED ON |
| APPLICATION | |

CONTRACT NO

| | |
|---|---|
| DWN | C SHELTON |
| ENGR | C SHELTON |

SEE JPL DATA MANAGEMENT SYSTEM FOR APPROVAL SIGNATURES AND DATES

**JET PROPULSION LABORATORY**
CALIFORNIA INSTITUTE OF TECHNOLOGY
PASADENA, CA 91109

TITLE: SCHEMATIC DIAGRAM, EDGE SENSOR DRIVE BOARD - DIRECT V5

| SIZE | DAI | DWG NO | REV |
|---|---|---|---|
| B | 23835 | 10398551 | C |

SCALE: 1/1 | UNCLASSIFIED | SHEET 1 OF 1

ALTIUM GENERATED
REV 05/2018
JPL_B_SH1